

INFORMATION DISCLOSURE STATEMENT		Atty Docket: 02EDI46652637 Serial No.: Not Yet Assigned 10/820,464 Applicant: STORM ET AL. Filing Date: Herewith Group: AU 2622					
U.S. PATENT DOCUMENTS							
Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date
/C.J.Q./	AA	6,133,563	10/17/00	Clark et al.	250	208.1	
/C.J.Q./	AB	6,323,479	11/24/01	Hynecek et al.	250	214 L	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
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	AL						
	AM						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub Class	Translation
	AN						
	AO						
	AP						
	AQ						
OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)							
/C.J.Q./	AR	Kavadias, A Logarithmic Response CMOS Image Sensor with On-Chip Calibration, IEEE Journal of Solid-State Circuits, Vol. 35, No. 8, August 2000, Pages 1146-1152, XP001075115					
	AS						
	AT						
EXAMINER: /Carramah Quiett/				DATE CONSIDERED: 05/22/2007			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.